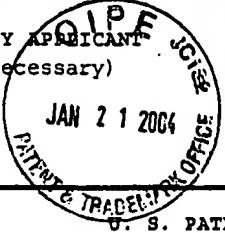


FORM PTO-1449 (REV. 7.80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 93-C-077C1 (STMI01-93077)		SERIAL NO. 09/712,827	
LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Loi N. Nguyen, et al.			
				FILING DATE November 14, 2000		GROUP 2835	



U. S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)	
LP ↓ ✓	AA	4,686,000	08/11/1987	Heath	156	643	Feb. 19, 1986
	AB	4,824,767	04/25/1989	Chambers, et al.	430	313	Jun. 2, 1987
	AC	5,063,176	11/05/1991	Lee, et al.	437	195	May 30, 1990
	AD	5,166,088	11/24/1992	Ueda, et al.	437	47	Jun. 25, 1991
	AE	5,266,516	11/30/1993	Ho	437	57	Jan. 2, 1992
	AF						
AG							

FOREIGN PATENT DOCUMENTS							
DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION		
					YES	NO	
LP ↓ ✓	AH	0 523 856 A2	06/28/91	Europe			
	AI	0 534 130 A1	3/31/93	Europe			
	AJ	JP60246675	06/12/85	Japan			
	AK	JP4196486	07/16/92	Japan			
	AL	JP5074958	03/26/93	Japan			
	AM						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)	
AN	"VLSI Fabrication Principles," by Sorab K. Ghandhi, Rensselaer Polytechnic Institute, A Wiley Publication, John Wiley & Sons, 1983, pages 479-482, 499-501.
AO	
AP	

EXAMINER <div style="font-size: 1.5em; font-family: cursive; margin-top: 10px;">Long Pham</div>	DATE CONSIDERED <div style="font-size: 1.5em; font-family: cursive; margin-top: 10px;">7/27/04</div>
--	---

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line if not in conformance and not considered. Include copy of this form with next communication to applicant.